

FIG. 1 (PRIOR ART)

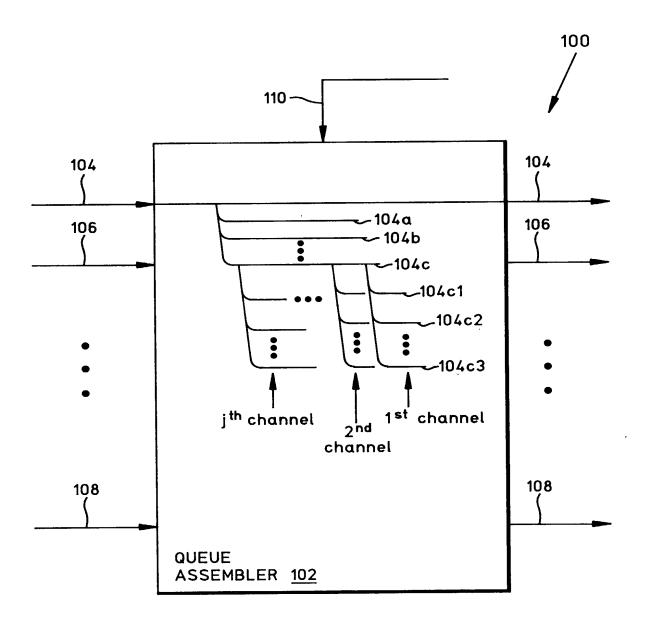
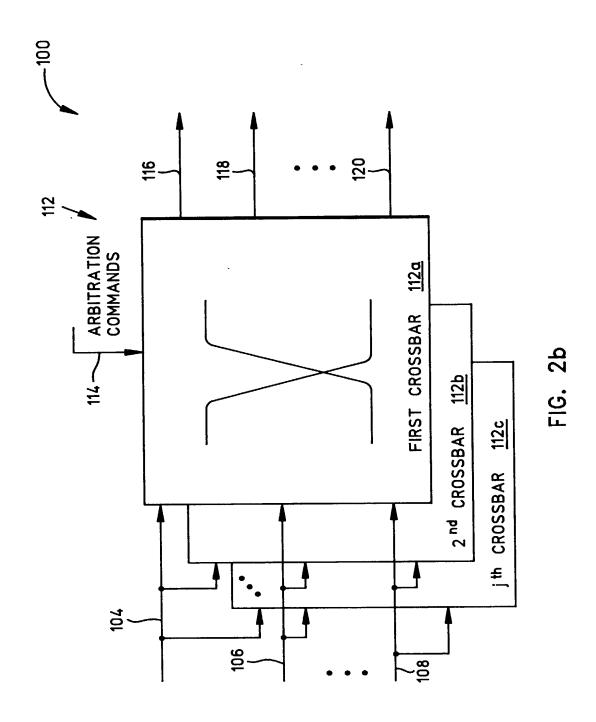
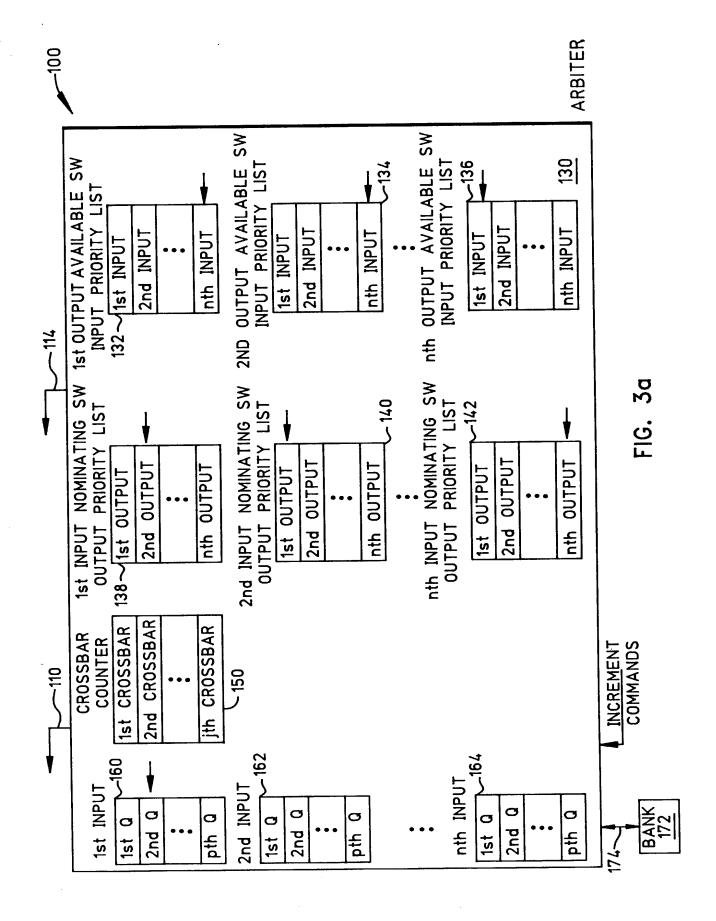
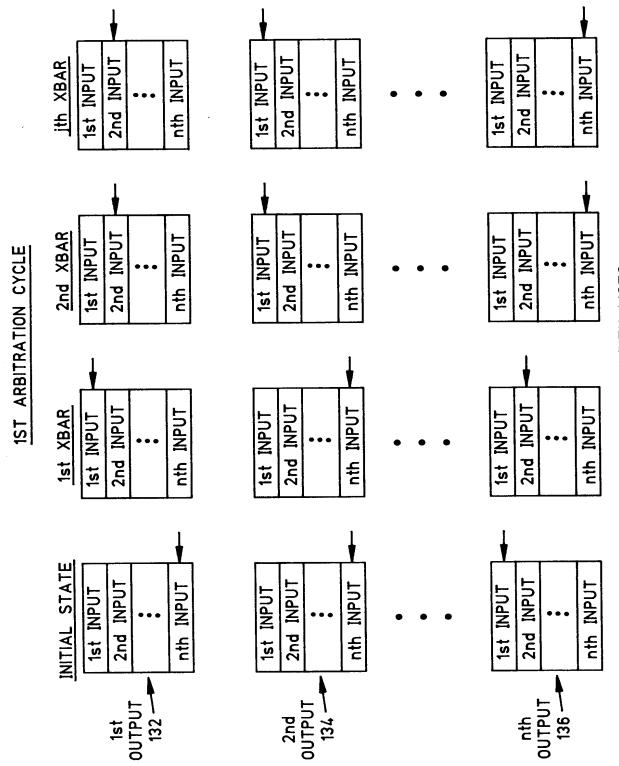


FIG. 2a

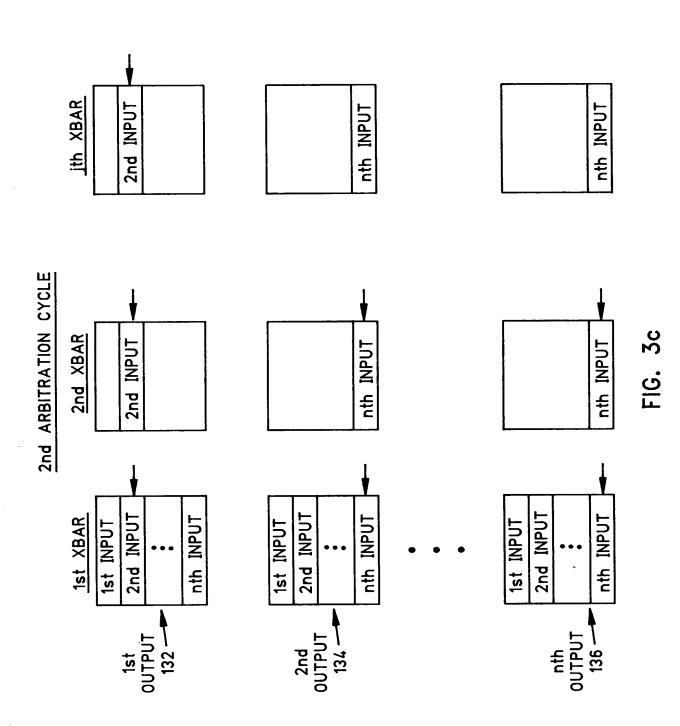


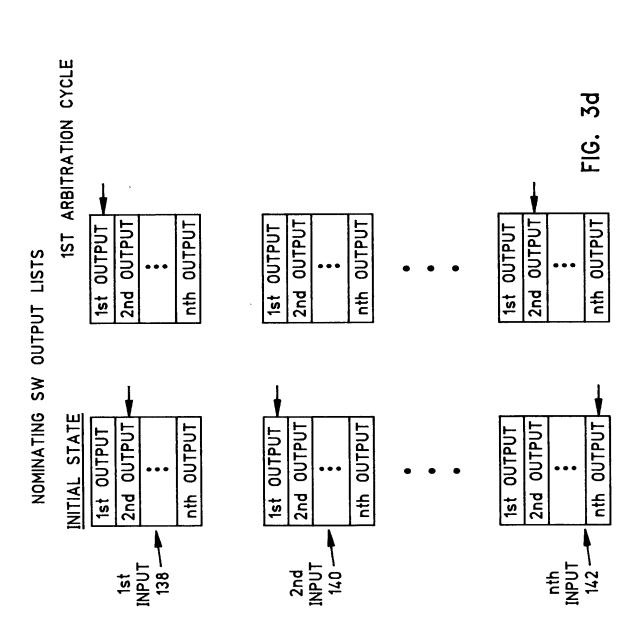


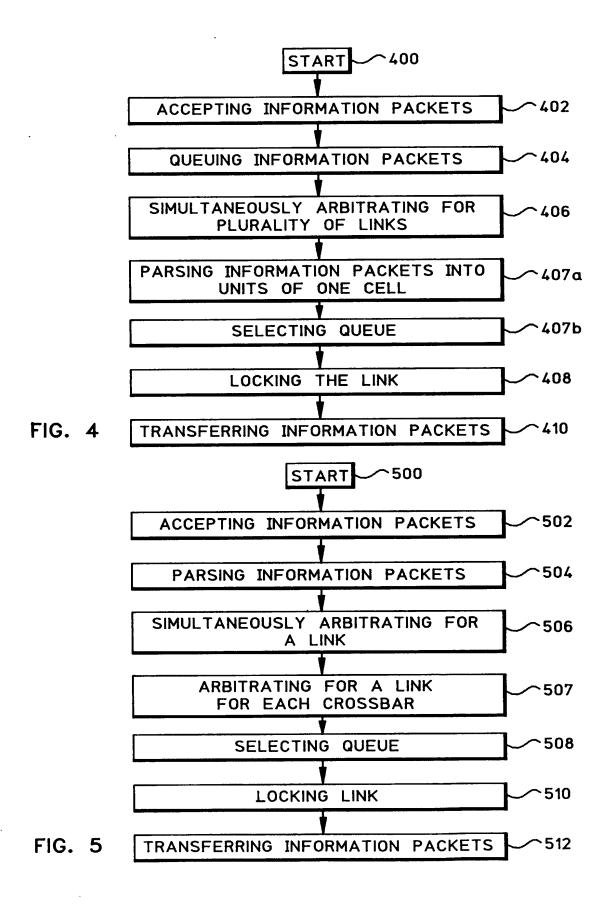


AVAILABLE SW INPUT PRIORITY LISTS

FIG. 3b







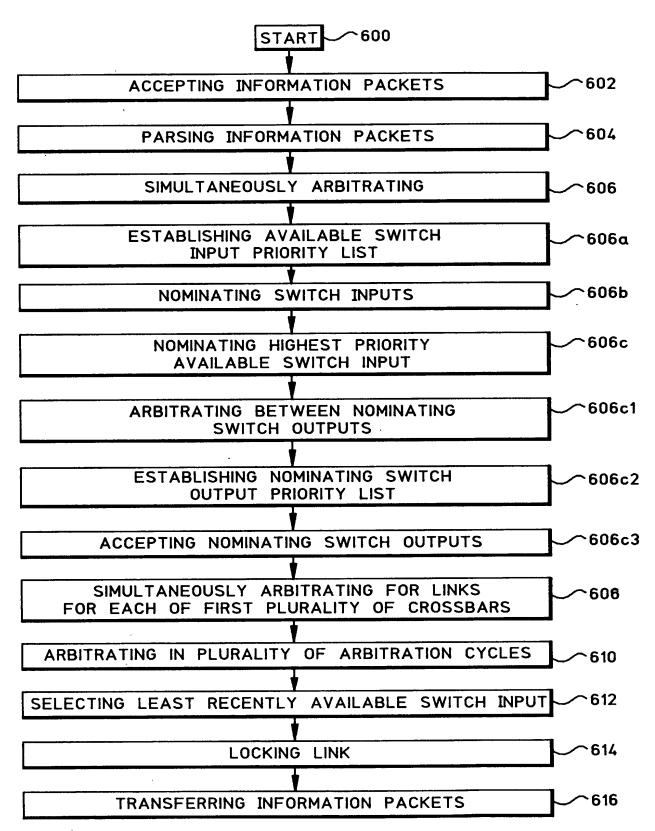
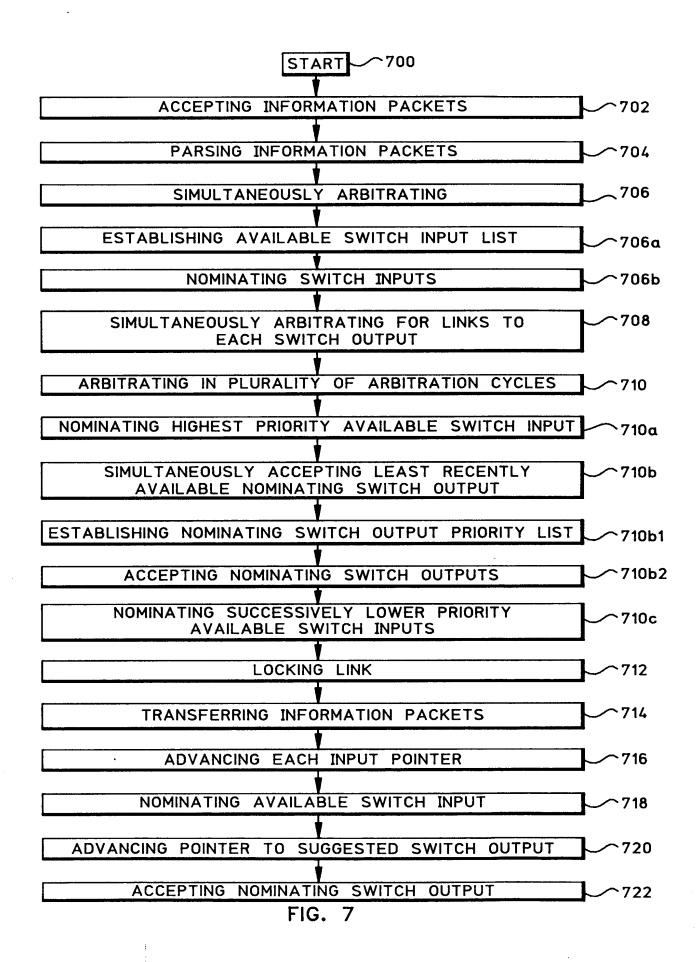


FIG. 6



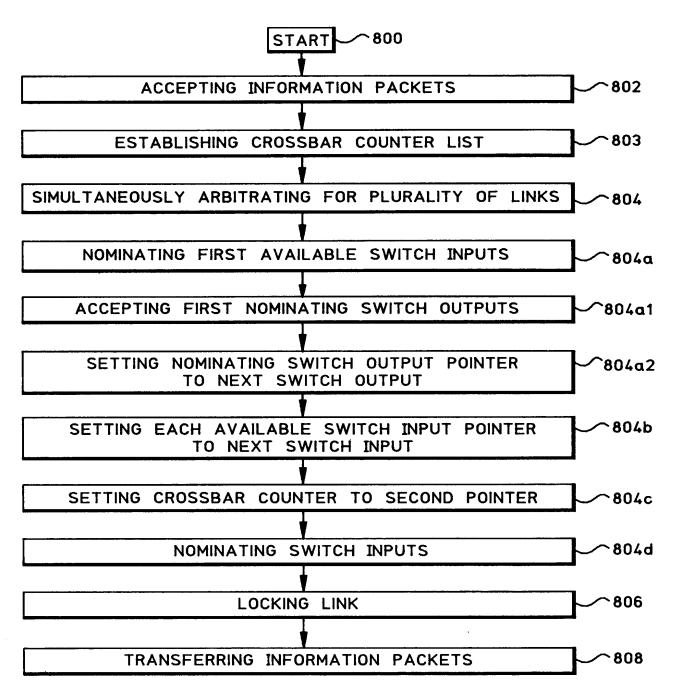


FIG. 8

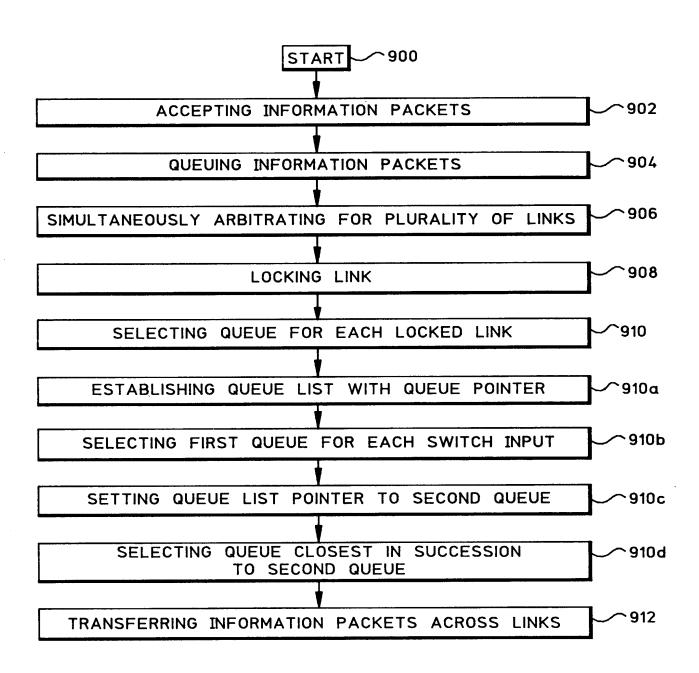


FIG. 9

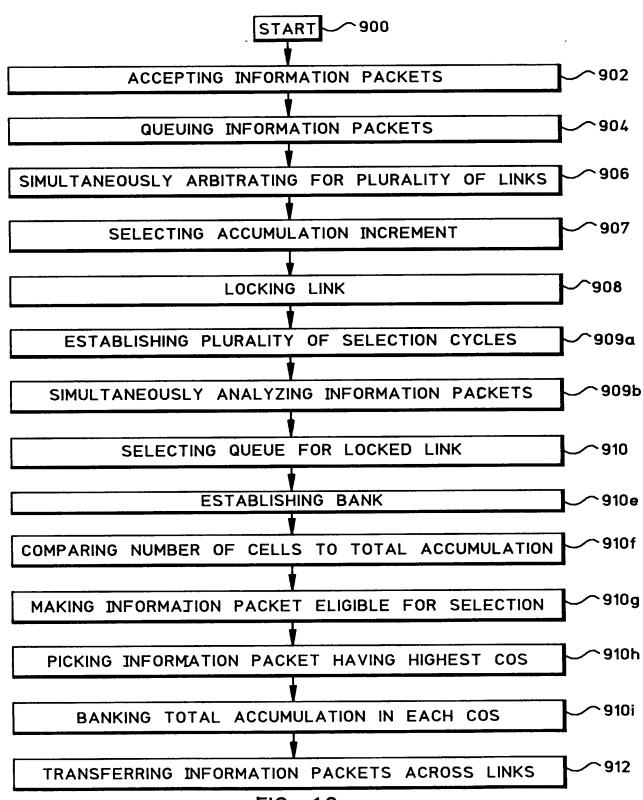


FIG. 10

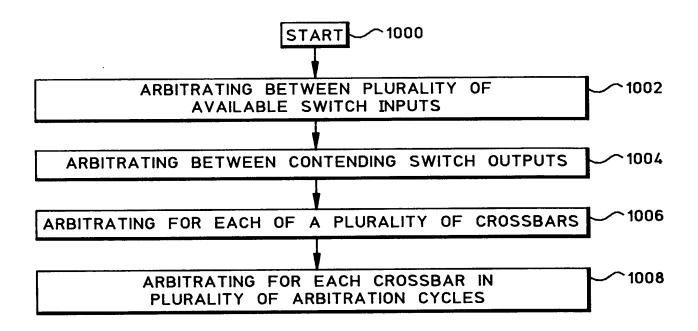


FIG. 11